

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: BHULLAR, Gurpreet; ALLAN, Graham
Serial No.: 10/702,502
Filed: November 7, 2003
Title: PROCESS, VOLTAGE, TEMPERATURE INDEPENDENT SWITCHED
DELAY COMPENSATION SCHEME
Group: 2611
Examiner: Kevin M. Burd
Attorney Ref.: PAT 2445B-2 US

October 19, 2007

Mail Stop AF

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, Virginia
22314-1450
U.S.A.

Dear Sir:

RESPONSE AND AMENDMENT UNDER 37 CFR 1.113(c)

This correspondence is in response to the Final Office Action dated July 20, 2007.
Terminal Disclaimers are submitted separate cover.

Amendments to the Claims begin on page **2** of this paper

Remarks/Arguments begin on page **7** of this paper.